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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Krishna et al.

Attorney Docket No.: BRCMP003

Application No.: 09/610,798

Examiner: Not yet assigned

Filed: July 6, 2000

Group: 2766

Title: Distributed Processing In A Cryptography
Acceleration Chip

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail to: Commissioner for Patents, Washington, DC 20231 on January 15, 2003.

Signed:

Latrice R. Williams
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INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)

Commissioner for Patents
Washington, DC 20231

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JAN 23 2003

Dear Sir:

Technology Center 2100

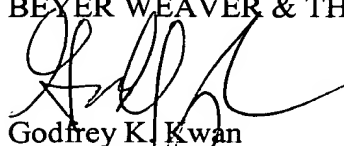
The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. BRCMP003).



Respectfully submitted,
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